

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): John S. Yates, Jr., et al.

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Title: COMPUTER FOR EXECUTING TWO DIFFERENT INSTRUCTION SETS

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COMMISSIONER FOR PATENTS

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PRELIMINARY AMENDMENT AND TRANSMITTAL OF FORMAL DRAWINGS

Kindly amend the application as follows.

In the Specification:

Please amend the specification as follows.

At page 9, lines 8-9, change "for translating address references is" to --translates address references--.

At page 33, lines 13-14, change "the I-cache" to --I-cache 112--.

At page 40, line 12, replace "the emulator" with --emulator 316--.

At page 40, line 13, replace "The emulator" with --Emulator 316--.

At page 41, line 14, replace "tapestry" by --Tapestry--.

At page 41, lines 28-29, replace "the Tapestry operating system and the X86 operating system" with --Tapestry operating system (312 of Fig. 3a) and X86 operating system 306--.

At page 42, line 1, replace "set" with --cleared--.

At page 42, lines 18 and 21, replace "XP bit" with --page's XP bit 184, 186--.

At page 43, lines 28 and 29, replace "EPC.PC" with --EPC.EIP--.

At page 47, line 8, replace "tapestry" with --Tapestry--.

At page 52, line 2, replace "EPC" with --EPC.EIP--.

At page 54, line 28, replace "the emulator and profiler contexts" with --context for emulator 316 and profiler 400 --.

At page 55, line 7, replace "systems," with --systems 306, 312,--.

At page 55, line 30, replace the second instance of "system" with --system 306--.

At page 56, line 29, replace "The emulator" with --Emulator 316--.

At page 59, line 26, replace "EPC" with --EPC.EIP--.

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on November 14, 2000 as First Class Mail in an envelope with sufficient postage addressed to The Commissioner for Patents, Washington D.C. 20231.

Joe E. Boos